ABOUT THE INSTITUTE

Madras Institute of Technology is one of the premier technical institutions started in the year 1949 by Shri. C. Rajam, an eminent industrialist. The institute was the result of a bold experiment in technical education as it introduced for the first time, totally unconventional Engineering courses such as Aeronautical Engineering, Automobile Engineering, Electronics Engineering and Instrumentation Engineering. It was merged with Anna University in the year 1978.

ABOUT THE DEPARTMENT

The Department of Electronics Engineering established in the year 1949, has its core strength in the leading areas of Electronics & Communication technology. The academic programmes offered in the Department are B.E (Electronics and Communication Engineering), M.E (Communication and Networking). The cutting edge research areas include Image Processing & Pattern Recognition, Communication Technologies, Network Security, Sensor Networks, Optical Communication, Signal Processing, and VLSI. The Department has collaborative partners from academia and industry both within India and worldwide.

LOCATION

Madras Institute of Technology is located in Chrompet, Chennai and close to Chrompet railway station.

ABOUT THE FDTP

The FDTP is intended to provide opportunity for young faculty members to enrich their knowledge in the area of Computer Networks.

This FDTP is an attempt to support colleges and faculty members to handle Computer networks.

The course will cover the following topics:

- Fundamentals and Link Layer
- Media Access - Ethernet, Wireless LAN
- Internetworking - IP, CIDR, ARP, DHCP, ICMP
- Routing - RIP, OSPF, BGP, DVMRP, PIM
- Transport Layer - UDP, TCP, QoS
- Application Layer - SMTP, POP3, IMAP, HTTP, DNS, SNMP

RESOURCE PERSONS

- Academics and Industry Experts

REGISTRATION

1. Name (in capital) :
2. Designation :
3. Qualification :
4. Department :
5. Professional experience (in years)
   Teaching :
   Industrial :
6. Address of the College/institution :
7. Have you handled the course, if so how many times ? :
8. Phone (Office) :
   (Mobile) :
9. E-mail :

I hereby declare that all the details furnished by me are true to the best of my knowledge.

Signature of the Faculty  Signature of the Head of the Institution
CERTIFICATE FROM HEAD OF THE INSTITUTION

Dr./Mr./Mrs. ___________________________

is an employee of our institution. He/She will be permitted to attend the programme if selected.

Signature of the Principal with seal

ABOUT THE REGISTRATION

- Number of seats is restricted to 25.
- Selection on first come first serve basis.
- Preference will be given to the young faculty based on eligibility criteria.
- Selected candidates will be intimated by e-mail.
- Confirmation of participation is to be made by email.
- No TA/DA will be paid.
- No Registration Fee.
- No accommodation will be provided.

Send the duly filled in Registration forms to the Coordinator(s) by post.

TARGETED PARTICIPANTS

- Faculty Members from Engineering Colleges affiliated to Anna University.

IMPORTANT DATES

Last date for the receipt of application 21.11.2016
Intimation of selection will be sent only through e-mail on or before 22.11.2016
Confirmation by Participants 23.11.2016

For further details, contact:
The Co-ordinator
Dept. of Electronics Engineering, MIT Campus, Anna University Chennai Chennai – 600 044
Phone : 044-22516084/6089/6243
E-mail: tsubashri@annauniv.edu

ONE WEEK FACULTY DEVELOPMENT TRAINING PROGRAMME

on

COMPUTER NETWORKS (CS 6551)


Co-ordinators
Dr. M. Kannan
Dr. T.Subashri

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CENTRE FOR FACULTY DEVELOPMENT
ANNA UNIVERSITY

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Madras Institute of Technology
Campus,
Anna University, Chennai – 600 044.